IN THE CLAIMS:

Please amend the claims as follows, substituting any amended claim(s) for the corresponding pending claim(s):

What is claimed is:

1. (currently amended) A method for forming an integrated circuit radio frequency transceiver with reduced harmonic interference from undesired coupling, comprising:

producing a sample of the integrated circuit radio frequency transceiver, the step of producing the sample further including:

_____forming a plurality of signal traces on a top metal layer, the plurality of signal traces being operably disposed to carry ingoing signals on a receive path or outgoing signals on a transmit path of the integrated circuit radio frequency transceiver; and

forming a ~~shorted~~ resistive block in line with at least one signal trace of the plurality of signal traces wherein the resistive block comprises an input and an output and at least one resistive element and at least one trace, wherein the at least one resistive element and the at least one trace of the resistive block are operably disposed in parallel between the input and output of the resistive block and wherein the at least one trace of the resistive block is operable to short the at least one resistive element;

_____evaluating real performance of the sample of the integrated circuit radio frequency transceiver;

_____determining whether harmonic interference may need to be reduced;

_____selecting a first trace of the at least one trace and removing a short across at least one shorted resistive element ~~resistor~~ of the resistive block operably disposed in line with the first trace to form an unshorted resistive block; and

_____producing the integrated circuit radio frequency transceiver with the unshorted resistive block.

2. (original) The method of claim 1 further including evaluating real performance of the sample with the removed short of the unshorted resistive block.

3. (original) The method of claim 2 further including evaluating real performance of the sample with the removed short of the unshorted resistive block.

4. (original) The method of claim 2 further including selecting a second trace of the at least one trace and removing a short across at least one shorted resistive block of the second trace to form an unshorted resistive block of the second trace.

5. (original) The method of claim 4 wherein the step of producing the integrated circuit radio frequency transceiver includes producing transceivers with the unshorted resistive block having at least two shorts removed.

6. (original) The method of claim 2 further including determining whether to increase a resistance value of the unshorted resistive block of the second trace.

7. (original) The method of claim 1 wherein the step of removing the short across at least one shorted resistor of the resistive block includes removing shorts across resistors that result in two resistors being coupled in series.

8. (original) The method of claim 1 wherein the step of removing the short across at least one shorted resistor of the resistive block includes removing shorts across resistors that result in two resistors being coupled in parallel.

9. (currently amended) A method for forming an integrated circuit radio frequency transceiver with reduced harmonic interference from undesired coupling, comprising:

producing a sample of the integrated circuit radio frequency transceiver, the step of producing the sample further including:

forming a plurality of traces on a top metal layer, the plurality of traces being operably disposed to carry receive path or transmit path signals of the integrated circuit radio frequency transceiver; and

forming a ~~shorted~~ resistive block in line with at least one signal trace of the plurality of signal traces wherein the resistive block ~~includes a plurality of series coupled resistors, each resistor of the plurality of series coupled resistors having a short across the resistor~~ comprises an input and an output and at least one resistive element and at least one trace, wherein the at least one resistive element and the at least one trace of the resistive block are operably disposed in parallel between the input and output of the resistive block and wherein the at least one trace of the resistive block is operable to short the at least one resistive element;

evaluating real performance of the sample of the integrated circuit radio frequency transceiver;

determining whether harmonic interference may need to be reduced;

selecting and removing a first short across a first resistor operably disposed in line with the first trace of the plurality of series coupled resistors to create an RC filter resulting from a coupling of parasitic capacitance of the trace and an unshorted first resistor; and

_____producing the integrated circuit radio frequency transceiver with the unshorted first resistor.

10. (original) The method of claim 9 further including evaluating real performance of the sample with the unshorted first resistor.

11. (original) The method of claim 10 wherein the unshorted first resistor is physically closest to an end of the at least one trace.

12. (original) The method of claim 10 further including removing a short across a shorted second resistor to form an unshorted second resistor wherein the unshorted second resistor is coupled in series with the unshorted first resistor.

13. (original) The method of claim 12 wherein the step of producing the integrated circuit radio frequency transceiver includes producing the integrated circuit radio frequency transceiver with the unshorted first and second resistors.

14. (original) The method of claim 10 further including determining whether to increase a resistance value of the at least one trace by removing a short across a third resistor coupled in series with the first and second resistors of the at least one trace.

15. (currently amended) A method for forming an integrated circuit radio frequency transceiver with reduced harmonic from undesired coupling, comprising:

producing a sample of the integrated circuit radio frequency transceiver, the step of producing the sample further including:

forming a plurality of traces on a top metal layer, the plurality of traces being operably disposed to carry receive path or transmit path signals of the integrated circuit radio frequency transceiver; and

forming a ~~shorted~~ resistive block in line with at least one signal trace of the plurality of signal traces wherein the resistive block ~~includes a plurality of parallel coupled resistors wherein a short is coupled across the parallel coupled resistors of the resistive block~~ comprises an input and an output and at least one resistive element and at least one trace, wherein the at least one resistive element and the at least one trace of the resistive block are operably disposed in parallel between the input and output of the resistive block and wherein the at least one trace of the resistive block is operable to short the at least one resistive element;

evaluating real performance of the sample of the integrated circuit radio frequency transceiver;

determining whether harmonic interference may need to be reduced;

selecting and removing the short across the parallel coupled resistors of the resistive block to create an RC filter resulting from a coupling of parasitic capacitance of the trace and unshorted parallel coupled resistors; and

producing the integrated circuit radio frequency transceiver with the unshorted parallel coupled resistors.

16. (original) The method of claim 15 further including evaluating real performance of the sample with the unshorted parallel coupled resistors.

17. (original) The method of claim 16 further including removing a first resistor of the parallel coupled resistors to increase the resistance of an unshorted resistive block.

18. (original) The method of claim 17 wherein the step of producing the integrated circuit radio frequency transceiver includes producing the integrated circuit radio frequency transceiver with the unshorted resistive block with at least one removed resistor.

19. (original) The method of claim 18 further including determining whether to increase a resistance value of the resistive block by removing a second resistor coupled in parallel within the resistive block.

20. (currently amended) An integrated circuit radio transceiver, comprising:

_____front end transceiver circuitry for receiving and transmitting wireless communication signals wherein the front end transceiver front-end processes received RF signals and converts the processed signals to digital signals and converts outgoing digital signals to analog and upconverts the outgoing analog signals to RF and amplifies outgoing RF signals;

_____wherein the front end transceiver circuitry includes a plurality of traces on a top metal layer, the plurality of traces being operably disposed to carry receive path or transmit path signals of the integrated circuit radio frequency transceiver; and

_____a ~~shorted~~ resistive block in line with at least one trace of the plurality of traces wherein the resistive block ~~includes a plurality of parallel coupled resistors and wherein a short is coupled across the parallel coupled resistors of the resistive block~~ comprises an input and an output and at least one resistive element and at least one trace, wherein the at least one resistive element and the at least one trace of the resistive block are operably disposed in parallel between the input and output of the resistive block and wherein the at least one trace of the resistive block is operable to short the at least one resistive element.

21. (currently amended) An integrated circuit radio transceiver, comprising:

_____front end transceiver circuitry for receiving and transmitting wireless communication signals wherein the front end transceiver front-end processes received RF signals and converts the processed signals to digital signals and converts outgoing digital signals to analog and upconverts the outgoing analog signals to RF and amplifies outgoing RF signals;

_____wherein the front end transceiver circuitry includes a plurality of traces on a top metal layer; and

a ~~shorted~~ resistive block in line with at least one trace of the plurality of traces wherein the resistive block ~~includes a plurality of parallel coupled resistors and wherein a short is coupled across the parallel coupled resistors of the resistive block~~ comprises an input and an output and at least one resistive element and at least one trace, wherein the at least one resistive element and the at least one trace of the resistive block are operably disposed in parallel between the input and output of the resistive block and wherein the at least one trace of the resistive block is operable to short the at least one resistive element.